

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listing, of claims in the application:

1. **(Currently Amended)** An image processing device comprising:

an SIMD (Single Instruction stream Multiple Data stream) calculating means unit for performing operations, such as motion compensation, motion prediction, DCT (Discrete Cosine Transform) processing, IDCT (Inverse Discrete Cosine Transform) processing, quantization, and reverse quantization by means of a pipeline operation unit ~~that can be program controlled by an outside unit~~;

a VLC (Variable Length Code) processing means unit for performing variable-length encoding processing and variable-length decoding processing according to a given encoding method;

an external data interface means for performing a data transfer between the image processing device and an outside unit;

an instruction memory ~~of RAM~~, for holding ~~instructions~~ an instruction to be processed ~~which correspond to various coding methods, and are downloaded from outside~~; and

a processor for decoding the instruction held by said instruction memory, and for performing a programmed control operation on said SIMD calculating means unit, said VLC processing means unit, and said external data interface, wherein

~~in said SIMD calculating means, a DCT unit and an IDT are integrated~~
said SIMD unit and said VLC unit are configured to be connected to an external
memory through internal data bus and an external data interface regardless of said
processor in order to be program-controlled by an outside unit.

2. (Cancelled)

3. (Original) The image processing device according to Claim 1, wherein said instruction memory is a ROM (Read Only Memory).